

# High Performance Schottky Rectifier

## 320A/100V

### FEATURES

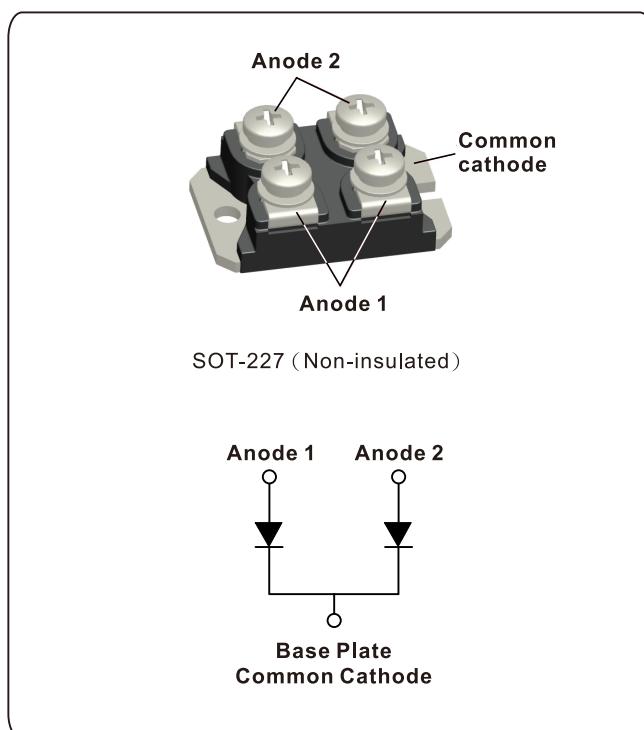
- 175°C  $T_J$  operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level
- International standard package SOT-227
- Low  $I_{RM}$  values

### DESCRIPTION

The NST320S100C Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

### TYPICAL APPLICATIONS

- High current switching power supplies
- Plating power supplies
- UPS system
- Converters
- Freewheeling diode
- Welder
- Reverse battery protection.



### PRODUCT SUMMARY

$I_F(AV)$	320A
$V_R$	100V

### MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNIT
$I_F(AV)$	Rectangular waveform	160 x 2	A
$V_{RRM}$		100	V
$I_{FSM}$	$t_p = 5 \mu s$ sine	12000	A
$V_F$	160 Apk, $T_J = 125^\circ C$	0.80	V
$T_J$	Range	-55 to 175	°C

### VOLTAGE RATINGS

PARAMETER	SYMBOL	NST320S100C	UNIT
Maximum DC reverse voltage	$V_R$	100	V
Maximum working peak reverse voltage	$V_{RWM}$		

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNIT		
Maximum average forward current per leg	$I_{F(AV)}$	50% duty cycle at $T_J = 100^\circ\text{C}$ , rectangular waveform			160	A		
Maximum peak one cycle non-repetitive surge current	$I_{FSM}$	5 $\mu\text{s}$ sine or 3 $\mu\text{s}$ rect. pulse	Following any rated load condition and with rated $V_{RRM}$ applied	12000	1400			
		10 ms sine or 6 ms rect. pulse						
Non-repetitive avalanche energy	$E_{AS}$	$T_J=25^\circ\text{C}$ , $I_{AS}=15\text{A}$ , $L=100\mu\text{H}$			11.3	mJ		
Repetitive avalanche current	$I_{AR}$	Current decaying linearly to zero in 1 $\mu\text{s}$ $f = 10 \text{ KHz}$ , $V_A=1.5 \times V_R$ typical			1.5	A		

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNIT		
Maximum forward voltage drop per leg	$V_{FM}^{(1)}$	160A	$T_J = 25^\circ\text{C}$	0.98	V			
		320A			1.21			
		160A	$T_J = 125^\circ\text{C}$	0.80				
		320A		1.05				
Maximum reverse leakage current per leg	$I_{RM}^{(1)}$	$T_J = 25^\circ\text{C}$	$V_R = \text{Rated } V_R$	2	mA			
		$T_J = 125^\circ\text{C}$			30			
Maximum junction capacitance per leg	$C_T$	$V_R = 5 \text{ V}_{\text{DC}}$ (test signal range 100 kHz to 1 MHz) $25^\circ\text{C}$			2500	pF		
Typical series inductance per leg	$L_S$	From top of terminal hole to mounting plane			6.0	nH		
Maximum voltage rate of change	$dV/dt$	Rated $V_R$			10000	V/ $\mu\text{s}$		

**Note**

(1) Pulse width < 500  $\mu\text{s}$ , duty cycle < 2%

THERMAL-MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Maximum junction and storage temperature range	$T_J, T_{Stg}$	-55	-	175	°C	
Thermal resistance, junction to case	$R_{thJC}$	-	-	0.30	°C/W	
Thermal resistance, case to heatsink	$R_{thCS}$	-	0.15	-		
Weight		-	30 (1.06)	-	g(oz.)	
Mounting torque, ± 10% to heatsink, M4 busbar, M4		-	1.1 (9.7)	-	N·m (lbf · in)	
		-	1.1 (9.7)	-		
Case style		JEDEC SOT-227 module (Non-insulated)				

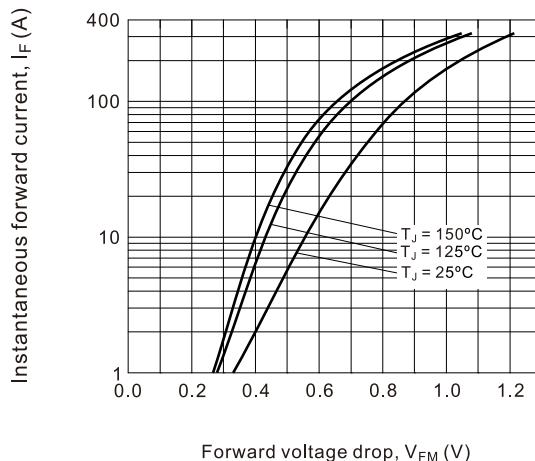
### Ordering Information Table

Device code

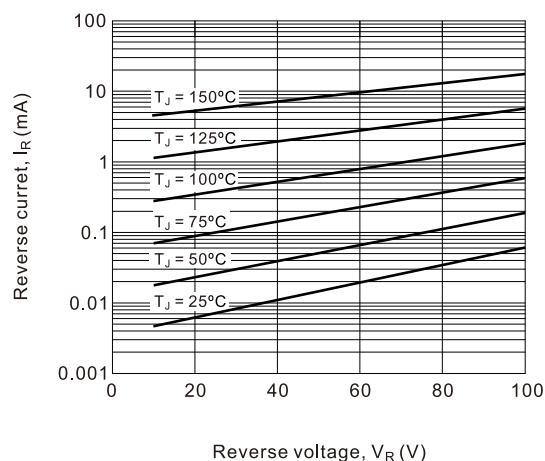
<b>N</b>	<b>ST</b>	<b>320</b>	<b>S</b>	<b>100</b>	<b>C</b>
1	2	3	4	5	6

- [1] - NPS's high power module
- [2] - Package indicator, "ST" for SOT-227
- [3] - Maximum average forward current, 320 = 160A x 2
- [4] - S = Schottky family
- [5] - Voltage rating (100 = 100V)
- [6] - Circuit configuration, Center tap common cathode, non-insulated

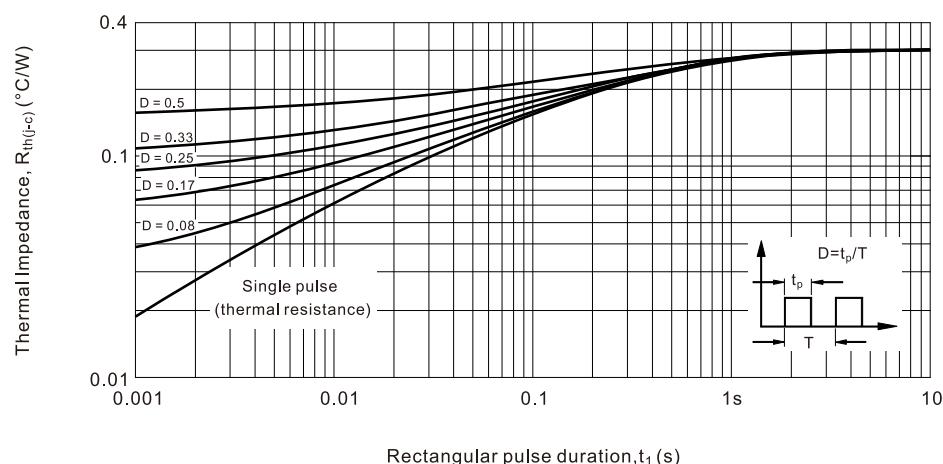
**Fig.1 Maximum forward voltage drop characteristics**



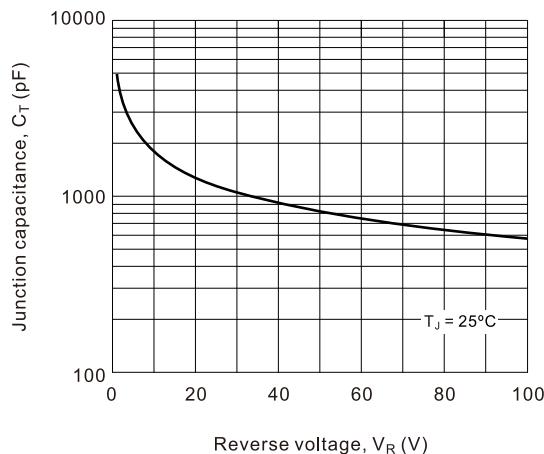
**Fig.2 Typical values of reverse current vs. reverse voltage**



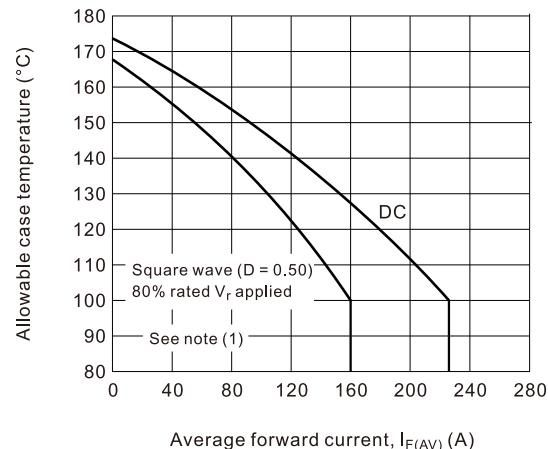
**Fig.3 Maximum thermal impedance  $R_{th(j-c)}$  characteristics**



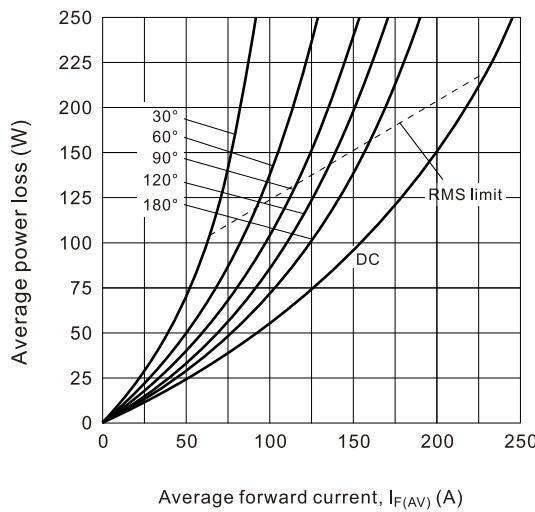
**Fig.4 Typical junction capacitance vs. reverse voltage**



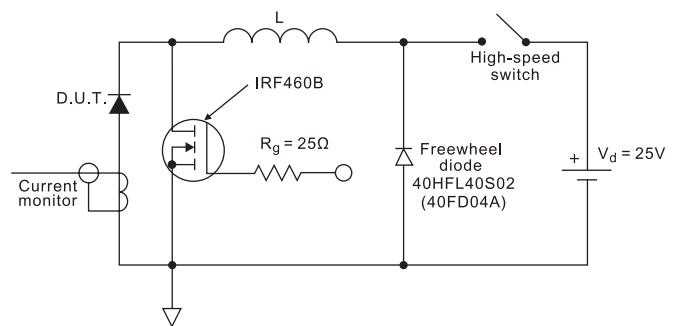
**Fig.5 Maximum allowable case temperature vs. Average forward current**



**Fig.6 Forward power loss characteristics**



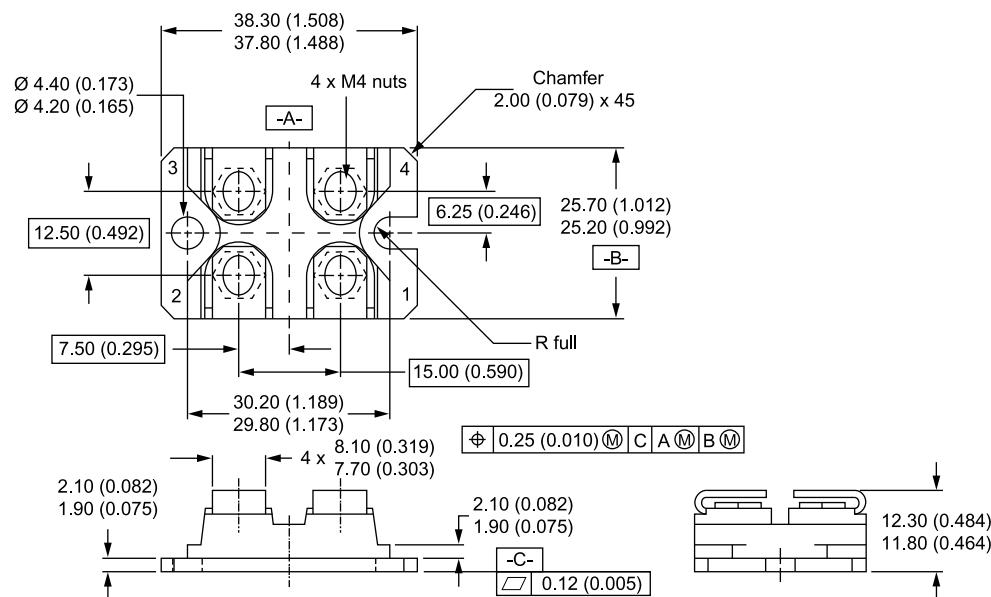
**Fig.7 Unclamped Inductive test circuit**



**Note**

(1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$   
 $P_d = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig.6)}$   
 $P_{dREV} = \text{Inverse power loss} = V_{R1} \times I_R (1-D); I_R \text{ at } V_{R1} = \text{rated } V_R$

SOT-227



All dimensions in millimeters (inches)

## Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
  - Controlling dimension: millimeter